

### Workshops / Seminars / Project Exhibitions Organized in CCOEW from 1 July 2017- 30 June 2018

Sr. No.	Name of the Faculty	Title /subject	Seminar / Conference /Workshop / STTP/Project Competition	Duration/ Date	Number of Faculty/Students attended that event
1	Dr. Anita Jain	Design and Implementation of backend VLSI using EDA tool	2 Days Workshop	13th & 14th July 2017	33 Students
2	Dr. Seema Rajput	Design and Implementation of backend VLSI using EDA tool	2 Days Workshop	13th & 14th July 2017	33 Students
3	Dr. Ashwini Deshpande	OpenCV and Python for Image and Video Processing Applications	Two days Workshop	03/10/2017 & 04/10/2017	15 Students + 3 Faculty
4	Prof. A.S.Khade	Design and Implementation of backend VLSI using EDA tool	2 Days Workshop	13th & 14th July 2017	33 Students
5	Prof. A.S.Khade	CMOS Mixed Signal and radio Frequency VLSI Design	Two Week	30th January 2017 to 4th Feb 2017	15
6	Prof. M.M.Dewasthale	Image and Computer Vision using Python and OpenCV	Two days Workshop	03/10/2017 & 04/10/2017	15 Students + 3 Faculty
7	Prof. R. T. Suryawanshi	Design and Implementation of backend VLSI using EDA tool	2 Days Workshop	13th & 14th July 2017	33 Students
8	Dr. Megha Borse	Outcome Based Education' Workshop	1 day Workshop	11th March 2018	35 Faculty
10	Supriya A Mangale	"PCB Designing and Fabrication "	2 day workshop	19 & 20 March 2018	109 students
11	Prof.(Mr.) M.S. Patankar		B.E.Project Competition ( Intra-departmental )	04-4-18 & 05-4-18	
14	Prof.S.G. Dube	"PCB Designing and Fabrication "	2 days workshop	19 & 20 March 2018	109 students

15	Prof. S.G. Dube	T.E. E & TC MINI PROJECT EXHIBITION CUM COMPETITION	1 day Exhibition cum Competition	3 April 2018	210 students
16	Prof.M.K.Pote	Organized Antenna Design Ansys HFSS workshop at	5 days Workshop	2-6th April 2018	25 faculties
17	Dr. Sharada N. Ohatkar	QualNET by Dr.Mrs. Radhika Joshi, COEP, Pune	1 day workshop	28th July 2017	10 Faculties 25 Students